



Analysis and Design of Digital Systems with VHDL

Allen Dewey

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Dewey presents digital engineering concepts and systems in the context of a top-down design process that moves from concept to realization using specific, step-by-step procedures. In other words, Dewey introduces and explains digital concepts and systems in the first three Parts of the book, and then demonstrates how VHDL is used to model these concepts and systems in the latter three Parts. As a result, this dual-track presentation helps readers build a critical new skill set increasingly required in industry - using a hardware description language (VHDL) to specify digital systems in formats that powerful computer-based design automation programs can process quickly, making design of complex digital systems easier and faster.

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